

WHAT IS CLAIMED IS:

1. A display device, comprising:
a plurality of pixel electrodes;
image signal lines for supplying data signals to
said pixel electrodes;
a plurality of scanning signal lines provided so as
to intersect said image signal lines;
a driving circuit for outputting a scanning signal
to actuate said scanning signal lines; and
thin film transistors each having a gate, a source,
and a drain which are connected with one scanning signal
line, one image signal line, and one image electrode,
respectively, said thin film transistors being provided
at the intersections of said image signal lines and said
scanning signal lines, respectively,
wherein said driving circuit controls falls of the
scanning signal.

2. The display device as set forth in claim 1,
wherein said driving circuit conducts control based on
signal delay transmission characteristics inherent in
each scanning signal line, so that the scanning signal
falls at a substantially same slope wherever on each
scanning signal line.

3. The display device as set forth in claim 2, wherein the falls of the scanning signal are sloped in an ON region of said thin film transistors.

4. The display device as set forth in claim 1, wherein said driving circuit controls the slopes of the falls of the scanning signal, based on gate voltage-drain current characteristics of said thin film transistors.

5. The display device as set forth in claim 4, wherein the falls of the scanning signal are sloped in an ON region of said thin film transistors.

6. The display device as set forth in claim 2, wherein said driving circuit further controls the slopes of the falls of the scanning signal, based on gate voltage-drain current characteristics of said thin film transistors.

7. The display device as set forth in claim 1, wherein:

the scanning signal is composed of a gate-on voltage which causes said thin film transistor to attain an ON state and a gate-off voltage which causes said thin film transistor to attain an OFF state; and

said driving circuit includes:

a shift register section composed of a plurality of flip-flops which are cascaded and to which a scanning timing control signal is supplied;

slope control sections for controlling the slopes of the falls from the gate-on voltage to the gate-off voltage; and

switch sections each of which switches the gate-on voltage for the gate-off voltage or vice versa according to an output of each flip-flop.

8. The display device as set forth in claim 7, wherein each slope control section includes a through-rate control element.

9. The display device as set forth in claim 1, wherein:

the scanning signal is composed of a gate-on voltage which causes said thin film transistor to attain an ON state and a gate-off voltage which causes said thin film transistor to attain an OFF state; and

said driving circuit includes:

a control section for outputting a discharge control signal which synchronizes with each scanning period; and

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a driving voltage generating section which usually generates the gate-on voltage, and discharges the gate-on voltage in response to the discharge control signal.

10. The display device as set forth in claim 9, wherein the voltage outputted from said driving voltage generating section has a serrature-like waveform.

11. The display device as set forth in claim 9, wherein a minimum value of the voltage outputted from said driving voltage generating section is substantially equal to a threshold voltage of said thin film transistors.

12. The display device as set forth in claim 9, wherein said driving voltage generating section includes a resistor and a capacitor.

13. The display device as set forth in claim 1, wherein:

the scanning signal is composed of a gate-on voltage which causes said thin film transistor to attain an ON state and a gate-off voltage which causes said thin film transistor to attain an OFF state; and

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said driving circuit includes:

a control section which outputs a charge control signal and a discharge control signal, which both synchronize with each scanning period;

a slope voltage control section which charges up in response to the charge control signal and outputs a slope control voltage, while makes the slope control voltage zero by discharging in response to the discharge control signal; and

a subtracting section which outputs a voltage resulting on subtraction of the slope control voltage from the gate-on voltage during the charging, while outputs the gate-on voltage during the discharge.

14. The display device as set forth in claim 13, wherein the voltage outputted from said subtracting section has a serrature-like waveform.

15. The display device as set forth in claim 13, wherein a minimum value of the voltage outputted from said subtracting section is substantially equal to a threshold voltage of said thin film transistors.

16. The display device as set forth in claim 13, wherein said subtracting section is composed of a

differential amplifying circuit including an operational amplifier.

17. A display method of carrying out display by supplying data signals to a plurality of pixel electrodes through image signal lines and actuating the pixel electrodes by supplying a scanning signal thereto through scanning signal lines which intersect the image signal lines, wherein during the actuation, falls of the scanning signal are controlled.

18. The display method as set forth in claim 17, wherein during the actuation, the scanning signal is controlled on the basis of signal delay transmission characteristics inherent in each scanning signal line, so that the scanning signal falls at a substantially same slope wherever on each scanning signal line.

19. The display method as set forth in claim 17, wherein during the actuation, slopes of the falls of the scanning signal are controlled on the basis of gate voltage-drain current characteristics of a plurality of thin film transistors provided at the intersections of the image signal lines and the scanning signal lines.

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20. The display method as set forth in claim 18, wherein during the actuation, furthermore, slopes of the falls of the scanning signal are controlled on the basis gate voltage-drain current characteristics of a plurality of thin film transistors provided at the intersections of the image signal lines and the scanning signal lines.

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